UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/847,991	05/02/2001	Jason Seung-Min Kim	NVID-P003124	5788
	45594 7590 11/19/2008 NVIDIA C/O MURABITO, HAO & BARNES LLP		EXAMINER	
TWO NORTH MARKET STREET THIRD FLOOR			MYERS, PAUL R	
SAN JOSE, CA 95113		ART UNIT	PAPER NUMBER	
			2111	
			MAIL DATE	DELIVERY MODE
			11/19/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)			
Office Action Summary		09/847,991	KIM ET AL.			
		Examiner	Art Unit			
		Paul R. Myers	2111			
Period fo	The MAILING DATE of this communication ap or Reply	pears on the cover sheet with the o	correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) 又	Responsive to communication(s) filed on 24.5	Sentember 2008				
•	This action is FINAL . 2b) ☐ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
٥,١	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	on of Claims					
· · ·	·					
•	Claim(s) <u>28-51</u> is/are pending in the application.					
	4a) Of the above claim(s) is/are withdrawn from consideration.					
•	5) Claim(s) is/are allowed.					
	Claim(s) <u>28-51</u> is/are rejected.					
•	Claim(s) is/are objected to.	or election requirement				
اـــا(٥	Claim(s) are subject to restriction and/o	or election requirement.				
Applicati	on Papers					
9)☐ The specification is objected to by the Examiner.						
10)	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority ι	ınder 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some coll None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
2) Notic 3) Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO/SB/08)	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal F	ate			
Paper No(s)/Mail Date 6)						

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 9/24/08 have been fully considered but they are not persuasive.

In regards to applicants argument that "Zucker fails to teach or suggest the limitations of 'wherein said first resource controller is further operable to implement respective buses for coupling said plurality of processors to said plurality of memory resources": This is clearly incorrect. See Zucker figure 8. and figure 1. According to Zucker switch network 84 connects up to 4 input buses to 8 output buses with two bit paths for connecting the plural processor buses to the memory buses and switch network 86 connects 8 input buses to up to 4 output buses with two bit paths for connecting the memory buses to the processor buses. See column 10 lines 54-64 and Column 9 lines 54-68.

In regards to applicants argument that "Zucker fails to teach or suggest the limitation of 'wherein said first resource controller is further operable to enable each of said plurality of processors to simultaneously access a different memory resource of said plurality of memory resources": Zucker teaches "a processor 10 may be locked to several device ports at the same time" Column 11 lines 28-43.

In regards to applicants argument that Zucker teaches a single processor accessing a single memory: This is clearly incorrect. Zucker teaches parallel processing occurs with the lock being used to prevent two processors from simultaneously accessing the same memory while allowing them to simultaneously access different memories (See Column 12 lines 18-32).

Application/Control Number: 09/847,991 Page 3

Art Unit: 2111

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 28-33, 35, 37-42, 44, 46-50 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Zucker et al PN 3,805,247.

In regards to claims 28, 37, 46, 48: Zucker et al teaches a system (Figure 8) comprising: a plurality of memory resources (memories attached to 76); a plurality of peripheral resources (Devices attached to 76); a plurality of processors (10); a memory controller coupled to said plurality of processors and said plurality of memory resources (80, 84 and 86), wherein said memory controller comprises a first resource controller (80) for controlling access of said plurality of processors to said plurality of memory resources, and wherein said first resource controller is further operable to implement respective buses for coupling said plurality of processors to said plurality of memory resources (84 and 86 are crossbar switches); and a peripheral controller (82, 84 and 86) coupled to said plurality of processors and said plurality of peripheral resources, wherein said peripheral controller comprises a second resource controller (82) for controlling access of said plurality of processors to said plurality of peripheral resources, and wherein said first resource controller is further operable to implement respective buses for coupling said plurality of processors to said plurality of peripheral resources (84 and 86 are crossbar switches).

Application/Control Number: 09/847,991 Page 4

Art Unit: 2111

In regards to claims 29, 38: Zucker et al teaches a timer component (Clock 20 and 53) coupled to the controllers to control timing of the controllers.

In regards to claims 30-32, 39-41, 49-50: Zucker teaches simultaneous accessing of different memories and different peripherals (Column 11 line 28-43 and Column 10 line 41 to column 11 line 59)

In regards to claims 33, 42, 47: Zucker teaches a semaphore (Lock Column 12 lines 18-32).

In regards to claims 35, 44: Zucker teaches the priority scheme being round robin (revolving priority Column 8 lines 11-21)

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 34, 43 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zucker et al PN 3,805,247 in view of Frankeny et al PN 5,949,982.

In regards to claims 34, 43: Zucker teaches the processors communicating through crossbar switches to memories and devices via cross bar switches. Zucker however does not expressly teach the processors being able to communicate with each other. Zucker does teach them both being able to access the lock to determine if the other processor is accessing the desired shared resource. Frankeny et al teaches a plurality of processors communicating to each

Art Unit: 2111

other and a plurality of memories and a plurality of I/O devices via a crossbar switch. It would have been obvious to allow the processors to also communicate with each other because this would have allowed for functions such as symmetrical multiprocessing.

5. Claims 36, 45, 51 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zucker et al PN 3,805,247.

In regards to claims 36, 45 and 51: Zucker teaches the claimed computer system. Zucker however does not teach the system is portable. Official notice is taken that portable computers are well known. It would have been obvious to a person of ordinary skill in the art at the time of the invention to include Zucker et al's design in a portable computer because this would have made it portable.

Conclusion

- 6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

however, will the statutory period for reply expire later than SIX MONTHS from the mailing

date of this final action.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Paul R. Myers whose telephone number is 571 272 3639. The

examiner can normally be reached on Mon-Thur 6:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Mark Rinehart can be reached on (571) 272-3632. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Paul R. Myers Primary Examiner

Art Unit 2111

/Paul R. Myers/

Primary Examiner, Art Unit 2111